GENERAL DESCRIPTION

The ML7000 (for +5V)/ML7001(for +3V) are single channel CMOS CODEC LSIs containing A/D and D/A conversion filters for voice signals ranging from 300Hz to 3400Hz. These LSIs feature single-rail power supply and low power consumption and are suitable for ISDN systems, digital wireless systems, digital PBX, etc. As for analogue output, ML7000-xx and ML7001-xx are able to drive a receiver handset directly.

FEATURES

- Single power supply: +4.75 ~ 5.25V: ML7000-xx
  +2.70 ~ 3.60V: ML7001-xx
- Operating temperature: -30 ~ +85°C
- Power consumption (ML7000-xx):
  Operating mode: 25mW typ. (VDD=5.0V)
  Power down mode: 0.03mW typ.
- ITU/T companding laws:
  ML7000-01/ML7001-01: µ/A-law, pin-selectable
  ML7000-02/ML7001-02: µ-law
  ML7000-03/ML7001-03: A-law
- Transmission characteristics conforms to ITU-T G.714
- Built-in PLL eliminates a master clock.
- Short frame synchronous timing operation.
- Adjustable transmit gain.
- Adjustable receive gain.
- Built-in reference voltage supply.
- Balanced analog output can directly drive a 600Ω line transformer.

- Package options:
  24-pin plastic SOP: ML7000-01MA/ML7000-02MA/ML7000-03MA (SOP24-P-430-1.27-K)
  20-pin plastic SSOP: ML7000-01MB/ML7000-02MB/ML7000-03MB (SSOP20-P-250-0.95-K)
  24-pin plastic SOP: ML7001-01MA/ML7001-02MA/ML7001-03MA (SOP24-P-430-1.27-K)
  20-pin plastic SSOP: ML7001-01MB/ML7001-02MB/ML7001-03MB (SSOP20-P-250-0.95-K)